| Parameter | Rating | Units |
| :--- | :---: | :---: |
| Blocking Voltage | 60 | $\mathrm{~V}_{\mathrm{P}}$ |
| Load Current | 120 | $\mathrm{~mA}_{\mathrm{rms}} / \mathrm{mA}_{\mathrm{DC}}$ |
| On-Resistance (max) | 16 | $\Omega$ |
| LED Current to operate | 1 | mA |

## Features

- $1500 \mathrm{~V}_{\text {rms }}$ Input/Output Isolation
- Arc-Free With No Snubbing Circuits
- No EMI/RFI Generation
- Immune to radiated EM fields
- Wave Solderable
- Small 8-Pin SOIC Package
- Tape \& Reel Version Available


## Applications

- Security
- Passive Infrared Detectors (PIR)
- Data Signaling
- Sensor Circuitry
- Instrumentation
- Multiplexers
- Data Acquisition
- Electronic Switching
- I/O Subsystems
- Medical Equipment—Patient/Equipment Isolation
- Aerospace
- Industrial Controls


## Pin Configuration



## Description

The CPC2317N is a miniature device with one independent normally-open (1-Form-A) solid state relay and one independent normally-closed (1-Form-B) solid state relay in an 8-pin SOIC package. It employs optically coupled MOSFET technology to provide $1500 \mathrm{~V}_{\text {rms }}$ of input to output isolation.

The optically coupled outputs, which use IXYS Integrated Circuits Division's patented OptoMOS architecture, are controlled by a highly efficient infrared LED.

This device uses IXYS Integrated Circuits Division's state of the art, double-molded vertical construction packaging to produce one of the world's smallest relays. It is ideal for replacing larger, less-reliable reed and electromechanical relays.

## Approvals

- UL Recognized Component: File E76270
- CSA Approval Pending
- EN/IEC 60950-1 Certified Component: TUV Certificate B 131282667003


## Ordering Information

| Part \# | Description |
| :--- | :--- |
| CPC2317N | 8-Pin SOIC (50/tube) |
| CPC2317NTR | 8-Pin SOIC (2000/reel) |

Switching Characteristics of Normally-Open (Form-A) Devices


Switching Characteristics of Normally-Closed (Form-B) Devices


## Absolute Maximum Ratings @ $25^{\circ} \mathrm{C}$

| Parameter | Ratings | Units |
| :--- | :---: | :---: |
| Blocking Voltage | 60 | $\mathrm{~V}_{\mathrm{p}}$ |
| Reverse Input Voltage | 5 | V |
| Input Control Current <br> Peak (10ms) | 50 | mA |
| Total Power Dissipation ${ }^{1}$ | 1 | A |
| Isolation Voltage, Input to Output | 600 | mW |
| Operational Temperature | -40 to +85 | $\mathrm{~V}_{\text {rms }}$ |
| Storage Temperature | $-40 \mathrm{to}+125$ | ${ }^{\circ} \mathrm{C}$ |
| Soldering Temperature (10 Seconds) | 260 | ${ }^{\circ} \mathrm{C}$ |

Derate linearly $5 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at $+25^{\circ} \mathrm{C}$, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

## Electrical Characteristics @ $25^{\circ} \mathrm{C}$

| Parameter | Conditions | Symbol | Min | Typ | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Output Characteristics |  |  |  |  |  |  |
| Load Current <br> Normally Open (Form-A) Continuous ${ }^{1}$ <br> Normally Closed (Form-B) Continuous ${ }^{1}$ <br> Peak | $\frac{\mathrm{I}_{\mathrm{F}}=1 \mathrm{~mA}}{\mathrm{I}_{\mathrm{F}}=0 \mathrm{~mA}}$ | $\mathrm{I}_{\mathrm{L}}$ | - | - | 120 | $m A_{\text {rms }} / m A_{\text {DC }}$ |
|  | $t=10 \mathrm{~ms}$ | LLPK | - | - | $\pm 350$ | $m A_{p}$ |
| On-Resistance ${ }^{2}$ | $\mathrm{I}_{\mathrm{L}}=120 \mathrm{~mA}$ | $\mathrm{R}_{\text {ON }}$ | - | - | 16 | $\Omega$ |
| $\begin{array}{\|l} \hline \text { Switching Speeds } \\ \text { Turn-On } \\ \text { Turn-Off } \end{array}$ | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}, \mathrm{~V}_{\mathrm{L}}=10 \mathrm{~V}$ | $t_{\text {on }}$ $t_{\text {off }}$ | $\stackrel{-}{-}$ | $\stackrel{-}{-}$ | 3 | ms |
| Off-State Leakage Current | $\mathrm{V}_{\mathrm{L}}=60 \mathrm{~V}_{\mathrm{P}}$ | $\mathrm{I}_{\text {LEAK }}$ | - | - | 1 | $\mu \mathrm{A}$ |
| Output Capacitance <br> Normally Open (Form-A) <br> Normally Closed (Form-B) | $\mathrm{I}_{\mathrm{F}}=0 \mathrm{~mA}, \mathrm{~V}_{\mathrm{L}}=50 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}, \mathrm{~V}_{\mathrm{L}}=50 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ | $\mathrm{C}_{\text {OUT }}$ | $\stackrel{-}{-}$ | 5 10 | $\stackrel{-}{-}$ | pF |
| Input Characteristics |  |  |  |  |  |  |
| Input Control Current to Activate ${ }^{3}$ | $\mathrm{I}_{\mathrm{L}}=100 \mathrm{~mA}$ | $I_{F}$ | - | 0.40 | 1 | mA |
| Input Control Current to Deactivate | - | $I_{F}$ | 0.1 | 0.35 | - | mA |
| Input Voltage Drop | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}$ | $V_{F}$ | 0.9 | 1.2 | 1.4 | V |
| Reverse Input Current | $\mathrm{V}_{\mathrm{R}}=5 \mathrm{~V}$ | $I_{R}$ | - | - | 10 | $\mu \mathrm{A}$ |
| Common Characteristics |  |  |  |  |  |  |
| Capacitance, Input to Output | $\mathrm{V}_{10}=0 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ | $\mathrm{C}_{10}$ | - | 1 | - | pF |

1 Load current derates linearly from $120 \mathrm{~mA} @ 25^{\circ} \mathrm{C}$ to $60 \mathrm{~mA} @ 85^{\circ} \mathrm{C}$, and must be derated for both poles operating simultaneously.
2 Measurement taken within 1 second of on-time.
3 For applications requiring high temperature operation (greater than $60^{\circ} \mathrm{C}$ ) a minimum LED drive current of 3 mA is recommended

## Common Performance Data @ $25^{\circ} \mathrm{C}$ (Unless Otherwise Noted)*



Typical Blocking Voltage
vs. Temperature
(Form-A $I_{F}=0 \mathrm{~mA}$, Form-B $\mathrm{I}_{\mathrm{F}}=2 \mathrm{~mA}$ )



## Form-A Performance Data @ $\mathbf{2 5}^{\circ} \mathrm{C}$ (Unless Otherwise Noted)*




*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

## Form-B Performance Data @ $\mathbf{2 5}^{\circ} \mathrm{C}$ (Unless Otherwise Noted)*


*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

## Manufacturing Information

Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingression. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, IPC/JEDEC J-STD-020, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a Moisture Sensitivity Level (MSL) rating as shown below, and should be handled according to the requirements of the latest version of the joint industry standard IPC/JEDEC J-STD-033.

| Device | Moisture Sensitivity Level (MSL) Rating |
| :---: | :---: |
| CPC2317N | MSL 3 |

## ESD Sensitivity

This product is ESD Sensitive, and should be handled according to the industry standard JESD-625.

## Soldering Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of J-STD-020 must be observed.

| Device | Maximum Temperature x Time | Maximum Refilow Cycles |
| :---: | :---: | :---: |
| CPC2317N | $260^{\circ} \mathrm{C}$ for 30 seconds | 3 |

## Board Wash

IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since IXYS Integrated Circuits Division employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after soldering processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.

## MECHANICAL DIMENSIONS

## CPC2317N



## PCB Land Pattern


$\frac{\text { Dimensions }}{\mathrm{mm}}$
(inches)

## CPC2317NTR Tape \& Reel



NOTES:

1. All dimensions in millimeters
2. 10 sprocket hole pitch cumulative tolerance $\pm 0.20$.
3. Carrier camber is within 1 mm in 250 mm .
4. Tape material : Black Conductive Polystyrene Alloy. 5. All dimensions meet EIA-481-C requirements.

6 . Thickness : $0.30 \pm 0.05 \mathrm{~mm}$.

For additional information please visit our website at: www.ixysic.com
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